



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
SANCHES ET AL.)
Serial No. **09/915,761**)
Confirmation No. **7474**)
Filing Date: **JULY 26, 2001**)
For: **DIGITAL SIGNAL PROCESSOR WITH**)
PARALLEL ARCHITECTURE)

)

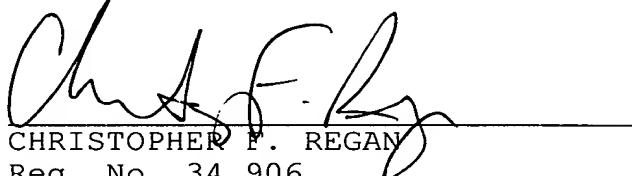
TRANSMITTAL OF FORMAL DRAWINGS

Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Enclosed are three (3) sheets of formal drawings to
be filed in the above-identified patent application.

Respectfully submitted,


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